

IN THE CLAIMS:

The status of the claims is as follows.

Claims 1-4 (canceled).

5. (Original) A circuit, comprising:
a transistor having a first terminal and a body;
a voltage detector that is to detect a terminal voltage that is to be applied to the first terminal of the transistor and that is to apply a bias voltage to the body of the transistor based upon the detected terminal voltage.

6. (Original) The circuit of claim 5, wherein the bias voltage is substantially equal to the terminal voltage.

7. (Original) The circuit of claim 5, wherein the bias voltage is less than the terminal voltage but not less than the terminal voltage drop across a parasitic diode in the body of the transistor when the transistor is in active mode.

Claims 8-30 (canceled).